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Examiner: Fernando L. Toledo	Total Pages Sent: 2
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

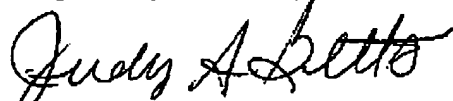
Applicants: Su, *et al.* Docket No.: TSM03-0150
Serial No.: 10/675,862 Art Unit: 2823
Filed: 9/30/2003 Examiner: Fernando L. Toledo
Title: Apparatus and Method for Manufacturing a Semiconductor Wafer with Reduced Delamination and Peeling

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- Facsimile Transmission Certification (1 page)
- Election (1 page)

Respectfully submitted,



Judy A. Betts
Legal Assistant

Confirmation Respectfully Requested

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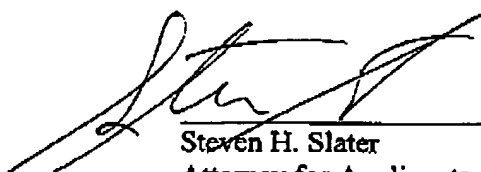
ELECTION

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed on March 18, 2005, Applicants hereby elect Group I, claims 1-46, with traverse.

Respectfully submitted,

April 8, 2005


Steven H. Slater
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